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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|------------------------------------|---|-------------------------|
| In re Patent Application of |) | Group Art Unit: 2811 |
| Ronald Pasqualini |) | Examiner: O. Nadav |
| Appln. No. 09/164,216 ✓ |) | <u>AMENDMENT</u> |
| Filed: September 30, 1998 |) | |
| For: ESD PROTECTION CIRCUIT |) | 2001 Ferry Building |
| UTILIZING FLOATING LATERAL |) | San Francisco, CA 94111 |
| CLAMP DIODES |) | (415) 433-4150 |

Assistant Commissioner for Patents
Washington, D.C. 20231

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on August 15, 2000.

LIMBACH & LIMBACH L.L.P.

Date: August 15, 2000

By: Norma E. Gillespie
Name: Norma E. Gillespie

Sir:

In response to the Official Action mailed May 15, 2000, please amend the above-identified application as follows:

In the Claims

Please cancel claims 2-4, 7-9, 16, and 24-31.

Please amend the claims as follows:

1. (Amended) A semiconductor chip having a substrate of a first conductivity type, the chip comprising:
a pad;
an electrostatic discharge (ESD) positive line, the ESD positive line not being connected to a steady voltage source;

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Cont.